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## UTILITY PATENT APPLICATION TRANSMITTAL

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Washington, DC 20231

Dated: November 20, 2000

Sir:

Transmitted herewith for filing is the utility patent application of:

Inventor(s): Jukka ALVE, Jan MÅRTENSSON, Ola LIDHOLM, Valtteri NIEMI, Juha TOMBERG, Pasi KÄRKÄS, Harri PEKONEN, Rami SUOMINEN

For: Content Protection Scheme For A Digital Recording Device

Enclosed are:

- Transmittal letter (2x) with Fee Computation Sheet
- General Authorization For Payment of Fees (2x)
- Title Page, Specification, Claims 1 to 34 & Abstract (25 pages [total number of pages of application])
- Unexecuted Declaration and Power of Attorney (6 p.)
- 5 sheet(s) of drawing(s) (Figs. 1 to 5)
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- Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Priority is claimed for this invention and application, corresponding applications having been filed in on, No. , respectively.

Respectfully submitted,  
COHEN, PONTANI, LIEBERMAN & PAVANE

By:



Michael C. Stuart  
Reg. No. 35,698

551 Fifth Avenue, Suite 1210  
New York, New York 10176  
Tel: (212) 687-2770  
Fax: (212) 972-5487

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## FILING FEE COMPUTATION SHEET

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BOX PATENT APPLICATION  
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In re Application of: **Jukka ALVE et al.**  
For: **Content Protection Scheme For A Digital Recording Device**

The filing fee has been calculated as shown below:

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TOTAL CLAIMS	<u>34</u> - 20 =	<u>14</u>	x 9 = \$	x 18 = \$ 252
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**APPLICATION FOR UNITED STATES  
LETTERS PATENT**

**CONTENT PROTECTION FOR A DIGITAL RECORDING DEVICE**

Inventors:

**Jukka ALVE  
Jan MARTENSSON  
Ola LIDHOLM  
Valtteri NIEMI  
Juha TOMBERG  
Pasi KARKAS  
Harri PEKONEN  
Rami SUOMINEN**

## **BACKGROUND OF THE INVENTION**

### **1. Field of the Invention**

The present invention relates to recording devices for digital data streams, and particularly to methods and systems for restricting distribution of copies recorded on such devices.

### **5 2. Description of the Related Art**

Digital recording technology makes it possible to easily reproduce exact copies of digital data streams such as audiovisual material, including movies, television programs, and audio recordings. Compression technologies, such as specified by the Motion Pictures Expert Group (MPEG), reduce the requirements for data storage space so that it is possible to store such recordings on a hard disk, such as the hard disk in a personal computer (PC). Once content is captured on a hard disk, it is simple to make large numbers of copies on writable compact disks (CD's) or DVD disks, for example, or to distribute the content to others over network connections, such as over the Internet.

Such copying capability poses the threat that illegal copying can become widespread, causing significant economic losses to the owners of proprietary rights to the content. There is thus a need to protect recorded content from illicit reproduction and distribution, i.e., copyright infringement.

## SUMMARY OF THE INVENTION

In accordance with the present invention, digitally recorded content is protected by encrypting the data prior to recording it on a hard disk using a standard encryption method with an encryption key that varies from one digital recorder unit to another. When the data is played back, it is decrypted with the same key. An attempt to play back the encrypted file on another 5 digital player or recorder unit will produce no intelligible output.

Manufacturing a number of recorders, each with a unique key completely hard-wired, is labor intensive and thus expensive. In the present invention, the key is formed by combining a relatively small portion hard-wired in the recorder and a large portion stored in a memory 10 associated with the recorder and retrievable under control of software.

In an alternative embodiment a sub-portion of the large portion is entered by a user. This enables the user to preclude other users of the same recorder who do not know the sub-portion from playing back certain program content.

An aspect of the invention provides a one-time-programmable device for storing the 15 encryption key, a first security bit, and a second security bit. A readout path is provided to retrieve the encryption key for purposes of testing after manufacture. When testing is complete, the first security bit is programmed ON (an irrevocable procedure). The readout path is arranged to be disabled by the ON condition of the first security bit. The readout path is further arranged to be re-enabled by programming ON the second security bit (also an irrevocable procedure); 20 however another path essential to functioning of the recorder is arranged to be disabled by the ON condition of the second security bit. Retrieval of the key to program it into a second

recorder to replace the first one if the first one has broken down is thus possible, but retrieval of the key for illicit purposes is thus discouraged.

Other objects and features of the present invention will become apparent from the following detailed description considered in conjunction with the accompanying drawings. It is to be understood, however, that the drawings are designed solely for purposes of illustration and not as a definition of the limits of the invention, for which reference should be made to the appended claims. It should be further understood that the drawings are not necessarily drawn to scale and that, unless otherwise indicated, they are merely intended to conceptually illustrate the structures and procedures described herein.

### **BRIEF DESCRIPTION OF THE DRAWINGS**

In the drawings, wherein like reference numerals denote similar elements throughout the several views:

Fig. 1 is a high-level block diagram of a system including a recorder embodying the

5 present invention;

Fig 2 shows further detail of a content protection device shown in the recorder of Fig. 1;

Fig. 3 depicts a memory address allocation scheme employed to enhance features of the  
present invention;

Fig. 4 depicts a basic scheme for recovering an internal key from the content protection  
device shown in Fig. 2; and

Fig. 5 depicts a scheme according to the present invention for recovering an internal key  
from the content protection device shown in Fig. 2.

## **DETAILED DESCRIPTION OF THE PRESENTLY PREFERRED EMBODIMENTS**

Fig. 1 is a high-level block diagram of a system embodying the present invention.

Recorder 1 includes digital processor 100 which receives a digital data stream, which for purposes of the present example is a digital television broadcast, but which might be digitized 5 audio, digitized video such as a movie or portion thereof, digitized information, etc.

Transmission of the digital data stream to digital processor 100 may be by means of radio-frequency transmission, cable, or the like. Digital processor 100 processes the digital data stream into a form intelligible to a user; in the present example a digital TV broadcast is presented on TV monitor 500.

10 The user of recorder 1 may direct that the digital data stream be recorded for later use.

Such recording may be on video cassette or rewritable compact disc (CD/R). Use of compression techniques such as MPEG-2 (a standard established by the Motion Pictures Expert Group) enables recording on a hard disk drive. In a present embodiment, recorder 1 is associated with a personal computer (PC) 400, with which a hard drive 420 is associated; Recorder 1 communicates with PC 400 via PCI bus 410 (so named because it conforms to the 15 Peripheral Component Interconnect bus standard) and through PC 400 recorder 1 communicates with hard drive 420. In an alternative embodiment, recorder 1 is a self-contained set-top box relative to TV monitor 500, in which case storage 420 and PC 400 are located in box housing recorder 1. Other embodiments may be contemplated that do not include a personal computer.

20 Such other embodiments would still have a storage device, such as hard drive 420.

Once program content is recorded on the storage device, copies of the content could then be distributed easily in the manner of transferring any PC files, such as making and distributing

multiple copies on multiple CD's, or distributing copies over the Internet 430. Since such distribution would be detrimental to owners of copyright in the content, the present invention stores encrypted copies in a manner adapted to limit such distribution.

Recorder 1 includes content protection device (CPD) 200 interposed between digital processor 100 and storage device 420 to perform the encryption and decryption. In a preferred embodiment, CPD 200 is a digital integrated circuit (ASIC). In a preferred embodiment, CPD 200 encrypts using the 3DES (data encryption standard) algorithm, with a 168-bit key. CPD 200 of any particular recorder 1 uses a particular key different from that used in other recorders 1. Thus, a copy given to the owner of another recorder 1 would not play back intelligibly on the other recorder 1.

Manufacturing such ASICs with unique on-chip 168-bit keys can be difficult and expensive. A preferred embodiment employs an alternative solution of using an externally applied unique or pseudo-unique key portion that can be stored in non-volatile memory such as FLASH memory 300, and an internal key portion "on chip" inside the said CPD 200. In a preferred embodiment the internal key portion is in a one-time-programmable ROM within CPD 200. In an alternative embodiment the internal key portion is encoded in bonding wires bonded during manufacture of the ASIC embodying CPD 200. The actual key used when encrypting and decrypting data is constructed as a combination of the external key and the internal key. The external key can be changed from time to time by software, for instance after each movie recorded. Key management software, running in digital processor 100, can maintain a table of the keys used in the FLASH memory, so that only indexes to the table need be passed to the CPD 200 to be attached to the file that contains the recording. The external keys can be made unique

for each unit, for example, by using the serial number of the unit as a seed for a key generation algorithm. Pseudo-unique keys can be generated by using a random number as the seed. Noise received by a satellite, cable, or terrestrial front-end that is not tuned to a proper channel can be used as a source for the random numbers. Variations to the internal key can be made during 5 packaging of the integrated circuit through, for example, the use of different patterns for bonding wires inside the device. This design makes it possible to manufacture small batches of devices with different internal keys for a lower cost, relative to manufacturing devices with the entire key 10 being unique.

At user direction, a sub-portion of the external key portion may be provided through user 10 interface 350. Using this feature, a user may restrict other users of the same recorder 1 from accessing certain program content. For example, a parent may provide a key sub-portion when recording material inappropriate for children, so that his minor children are then unable to view 15 that material because they do not know the sub-portion of the key.

Fig. 2 is a block diagram showing internal detail of CPD 200. (CPD 200 includes 15 software-controlled logic (not shown) for effecting intercommunication among CPD 200's various components.) Digital processor interface 202 maintains two-way communication between CPD 200 and digital processor 100, as well as with flash memory 300 and user interface 350. Dual-port random-access memory (DPRAM) 210 stores data being transferred into or out 20 of CPD 200, and is heavily used to transfer software-generated control messages between digital processor 100 and PC 400. Interrupt logic 212 passes interrupt signals between digital processor 100 and PC 400. A major use of such interrupt signals is to alert PC 400 to the availability of encrypted data during recording, and to the need for more data retrieval during playback. Control

and status registers 208 store control information and transfer control information, such as key components, into and out of CPD 200. Internal key 204 represents the 8-bit portion of the 168-bit 3DES key used for encryption and decryption. Key logic 206 combines the 160-bit external key portion obtained from flash memory 300 (or alternatively, from flash memory 300 in conjunction with user interface 350) with the 8-bit internal key 204 to produce, according to a predetermined relationship, the 168-bit 3DES key.

A current portion of a digital data stream (such as program content of a digitized TV broadcast) sent from digital processor 100 for storage on hard drive 420 is input to encrypter input FIFO 214, from which the payload portion is input to 3DES encrypter 218, along with an encryption key from key logic 206. Packet headers bypass encrypter 218 under control of encryption control logic 224 and are passed by multiplexer 226 along with the encrypted payload portion of the data stream. The resultant stream is stored in encrypter output FIFO 230 for subsequent output through PCI interface 250 to PCI bus 410 for transfer to hard drive 420. Keeping the header portions unencrypted facilitates such functions as fast forward and fast rewind.

A data stream retrieved from hard drive 420 is passed over PCI bus 410 and through PCI interface 250 into decrypter input FIFO 232. It is input to 3DES decrypter 228 along with a decryption key from key logic 206. The encrypted payload portion, combined with unencrypted packet headers, are combined by multiplexer 220 under control of decryption control logic 222, and the resultant data stream is input to decrypter output FIFO 216 for transmission through digital processor interface 202 to digital processor 100 for further transmission, such as to TV monitor 500.

The encryption is performed on 188-byte MPEG-2 Transport Packets in such a fashion that the 4-byte packet headers are kept unencrypted, while the remaining 184 bytes of the packet (often referred to as the payload) are encrypted. This provides the possibility to demultiplex the encrypted content into video and audio packets, for instance, according to the packet ID (PID) in the header, without having to perform decryption. It will also be possible to scan the file back and forth (fast forward and fast rewind) without sending unnecessary information through the decrypter to the video and audio decoders in digital processor 100, as it is possible to find the boundaries of packetized elementary stream (PES) packets according to the Payload Unit Start Indicator bit in the header. Audio packets can be skipped altogether in these modes. The encryption device performs a check for the packet length, and alerts the software controlling CPD 200 by an interrupt if an attempt is made to transfer packets that deviate from the standard 188 bytes.

A feature of the preferred embodiment facilitates replacing packet headers during retrieval of a stored data stream. As shown in Fig. 3, the output of encrypter output FIFO 216 (serial bytes) is mapped to occupy a plurality of addresses in address space, and that plurality of addresses is contiguous with the addresses of DPRAM 210. A packet is written to encrypter output FIFO 216 as usual. The header is read out by software, and a replacement header is written to top locations of DPRAM 210. A DMA transfer is initiated, spanning from the replacement header in the DPRAM 210 through the address range representing the number of payload positions to be read out of the FIFO 216. Synchronizing the advancing of the payload stream from FIFO 216 with reading out of the designated address range causes the replacement

header and the remaining part of the packet to be transferred to its destination in one DMA operation.

The invention solves the problem of illegal copying and distribution of digital media content, while still allowing the user to make a recording for his or her own, local use by 5 encrypting the recording with a key unique to the particular recording device, obviating playback on other devices which would have different keys.

The combination of using external and internal keys allows flexibility in key management. The external key can be changed by software from time to time, e.g., after each 10 movie recorded. An external key alone would provide only a limited amount of security, because it would be possible for a person trying to circumvent the content protection scheme to observe the external key on the bus through which it is loaded to the encryption / decryption device, and then decrypt the content of the hard disk, for instance by running DES or 3DES decryption software. In order to discourage people from distributing the content in the encrypted form along 15 with the external key(s), the internal key varies from one recorder 1 to another, making it hard to find another decryption device that can decrypt the content.

Keeping the transport packet headers unencrypted is an essential feature that allows easy implementation of trick modes such as fast forward and fast rewind. If the entire packets were encrypted it would be necessary to perform decryption for all the data that is skipped during playback, too.

20 The packet header and payload length security checks prevent abuse of the above described feature.

The packet header replacement feature, using a single DMA spanning from DPRAM to FIFO, is faster than copying the entire packet into memory, performing the replacement there, and then using DMA to transfer the packet with the replaced header to the audio decoder. This invention solves a problem that is specific to the audio decoder solution used, which dictates that

5 DMA must be used to place data to the audio decoder's input buffer.

The encryption protection provided by the present invention, however, might work against the rightful owner of the copies in the event that his recorder 1 breaks down. If he obtains another recorder 1 it will have a different internal key 204, so he will not be able to play his recorded copies on it. A procedure for recovering the internal key 204 so that it can be

10 programmed into another recorder 1 is possible, but is subject to abuse. Such a scheme is found in Fig. 4, which shows further detail of one-time programmable ROM 203 (introduced in Fig. 2) and the path from it to key logic 206. One-time programmable ROM 203 has at least eight bits, OTP1 through OTP8. Each of these eight bits is input to one of the eight AND gates 604, shown schematically as a single gate with the legend "x8". The gates are provided on the ASIC that

15 comprises CPD 200. A signal directing that the internal key be passed to key logic 206 is provided at the other input of each of the eight AND gates 604, and the eight-bit key is passed through to key logic 206. As noted above in connection with Fig. 2, key logic 206 passes the key and/or derivative information to encrypter 218 and decrypter 228.

In a preferred embodiment, there is also provision for connecting an external IC tester

20 700. The tester may provide on line 710 a signal requesting the internal key, which is applied to an input of each of eight AND gates 602, each of which has its other input connected to a different bit of internal key 204. Internal key 204 is passed to the tester on lines 720, and the

tester may then determine that one-time programmable ROM 203 was programmed correctly as intended. The connections 710 and 720 could be used by a device other than tester 700 for requesting and obtaining internal key 204, and thus could be used by a device to retrieve internal key 204 which could then be programmed into another recorder 1 for use by a rightful owner whose original recorder 1 has broken down. However, that facility could also be used by a pirate wanting to gain illicit access to the copies on hard drive 420 for playback on a different recorder 1.

Fig. 5 shows another embodiment using two additional bits available from one-time programmable ROM 203, namely OTP9 and OTP10. The AND gates corresponding to 602 of Fig. 4 are here provided with an additional input each, and are named 602A. OR gate 606 is introduced in Fig. 5. During initial manufacture of a recorder 1, an internal key 204 is programmed into bits OTP1 through OTP8. OTP9 and OTP10 are not programmed to values of 1, and thus contain values of 0. The 0 state of OTP9 conditions OR gate 606 to enable an input of AND gates 602A, which can then function as described in connection with Fig. 4 to pass internal key 204 to tester 700 on lines 720.

When testing of CPD 200 is complete, bit OTP9 is programmed to a value of 1. OR gate 206 is then not conditioned, and AND gates 602A thus can not be enabled to pass the internal key out on lines 720. Anyone wishing to access the copies on hard drive 420 is thwarted. There is no effect on any other portion of recorder 1, so recorder 1 functions normally for its rightful owner.

In the event that recorder 1 breaks down and its owner wishes to obtain another one, it will be necessary to retrieve internal key 204 from his old unit in order to program it into one-

time programmable ROM 203 of his new unit, so that he will be able to play back copies that he had recorded using his old unit. A service facility programs bit OTP10 of the old recorder 1 to a value of 1, which conditions OR gate 606 to enable AND gates 602A, permitting output of the key on lines 720 so that it can be programmed into one-time programmable ROM 203 of the new recorder 1. However, under the present invention the ON or 1 state of OTP10 irrevocably disables the normal operation of the recorder 1. The actual manner of doing this is a design choice; in the example given in Fig. 5, the disabling is accomplished by disabling the recording and playback paths of CPD 200. As shown in Fig. 2, encrypter 218 and decrypter 228 are integral to the recording and playback paths respectively. As shown in Fig. 5, bit OTP10 is required to be in the OFF or 0 state to enable encrypter 218 and decrypter 228; programming bit OTP10 to the 1 state in order retrieve the key thus disables the operation of the recorder 1. Since programming bit OTP10 to the 1 state is an irrevocable operation, operation of recorder 1 is permanently disabled. Many alternative ways may be contemplated in which the 1 state of bit OTP10 may be deployed to disable the recorder 1.

Thus, while there have been shown and described and pointed out fundamental novel features of the invention as applied to a preferred embodiment thereof, it will be understood that various omissions and substitutions and changes in the form and details of the devices illustrated, and in their operation, may be made by those skilled in the art without departing from the spirit of the invention. For example, it is expressly intended that all combinations of those elements and/or method steps which perform substantially the same function in substantially the same way to achieve the same results are within the scope of the invention. Moreover, it should be recognized that structures and/or elements and/or method steps shown

and/or described in connection with any disclosed form or embodiment of the invention may be incorporated in any other disclosed or described or suggested form or embodiment as a general matter of design choice. It is the intention, therefore, to be limited only as indicated by the scope of the claims appended hereto.

What is claimed is:

1. A method of recording digital data, comprising:  
2 receiving packets of a digital data stream in a recording and playback device;  
3 encrypting the packets in the recording and playback device according to an encryption  
4 key unique to the recording and playback device; and  
5 storing the encrypted packets.

1 2. The method of claim 1, further comprising:  
3 retrieving the encrypted packets;  
4 decrypting the encrypted packets in the recording and playback device according to the  
5 encryption key unique to the recording and playback device; and  
6 passing the decrypted packets to a presentation device;  
7 whereby a copy of the encrypted packets will not play back intelligibly using other  
recording and playback devices,  
8 whereby the encrypted packets are protected against unauthorized distribution.

1 3. The method of claim 1, wherein:  
2 a first portion of said encryption key unique to a recording and playback device is hard-  
3 wired in said recording and playback device;  
4 a second portion of said encryption key unique to said recording and playback device is  
5 stored in a memory associated with said recording and playback device;  
6 said encryption key is formed in said recording and playback device according to a  
7 predetermined algorithm from said first portion and said second portion.

1           4.       The method of claim 1, wherein:

2           a first portion of said encryption key unique to a recording and playback device is hard-  
3       wired in said recording and playback device;  
4           a plurality of second portions of said encryption key unique to said recording and  
5       playback device are stored in a memory associated with said recording and playback device;  
6           said encryption key is formed in said recording and playback device according to a  
7       predetermined algorithm from said first portion and one certain second portion from said  
8       plurality of second portions.

1           5.       The method of claim 1, wherein:

2           a first portion of said encryption key unique to a recording and playback device is hard-  
3       wired in said recording and playback device;  
4           a second portion of said encryption key unique to said recording and playback device is  
5       stored in a memory associated with said recording and playback device;  
6           a third portion of said encryption key is entered by a user of the recording and playback  
7       device; and  
8           said encryption key unique to said recording device is formed in said recording and  
9       playback device according to a predetermined algorithm from said first portion, said second  
10      portion, and said third portion,  
11      whereby another user of said recording and playback device who does not know said  
12      third portion is precluded from obtaining intelligible playback of a recorded data stream.

1           6.       The method of claim 1, wherein:

2           a packet comprises a first predetermined number of header bytes and a second  
3       predetermined number of payload bytes;  
4           the header bytes are stored unencrypted; and  
5           the payload bytes are stored encrypted,  
6           whereby operations performable on the data stream that require access to header bytes  
7       but not to payload bytes are performable on the recorded data stream.

1           7.       The method of claim 6, wherein stored packets are retrieved, header bytes  
2        are not decrypted, and payload bytes are decrypted..

1           8.       The method of claim 6, wherein stored packets are retrieved, header bytes  
2        are replaced, and payload bytes are decrypted.

1           9.       The method of claim 8, wherein:

2        the recording and playback device includes a random-access memory having a plurality  
3        of byte locations each corresponding to a different one of a plurality of first memory addresses;

4        bytes constituting a packet of the decrypted data stream appear sequentially at the output  
5        of a FIFO memory;

6        the output of the FIFO memory is mapped to a number of second memory addresses at  
7        least equal to the number of bytes comprising a packet;

8        the second memory addresses are contiguous with the first memory addresses;

9        the replacement header bytes are loaded into the n highest locations of the random-access  
10      memory, where n is the first predetermined number;

11      readout of the packet is accomplished by reading a number of the memory addresses  
12      equal to the sum of the first and second predetermined numbers commencing from the random-  
13      access memory address where the replacement header bytes commence.

1           10.      The method of claim 2, further comprising: arranging a readout path for  
2        at least a portion of the encryption key to be disabled by a first irrevocable condition and to be  
3        re-enabled by a second irrevocable condition, and  
4        arranging a path essential to functioning of the recording and playback device to be  
5        disabled by said second irrevocable condition.

1           11. The method of claim 10, comprising providing in the recording and  
2 playback device a one-time-programmable ROM, and wherein said first irrevocable condition  
3 comprises programming ON a first certain bit of the one-time programmable ROM, and wherein  
4 said second irrevocable condition comprises programming ON a second certain bit of the one-  
5 time programmable ROM.

1           12. The method of claim 11, further comprising storing said key portion bits  
2 in the one-time programmable ROM.

1           13. Apparatus for recording and playing back digital data, comprising:  
2           a data receiver for receiving packets of a digital data stream;  
3           an encrypter for encrypting the packets according to an encryption key unique to the  
4 apparatus; and  
5           a data store for storing the encrypted packets.

1           14. The apparatus of claim 13, further comprising:  
2           a decrypter for retrieving and decrypting the encrypted packets according to the  
3 encryption key unique to the apparatus; and  
4           a data transmitter for passing the decrypted packets to a presentation device;  
5           whereby a copy of the encrypted packets will not play back intelligibly using a different  
6 apparatus,  
7           whereby the encrypted packets are protected against unauthorized distribution.

1           15. The apparatus of claim 13, further comprising:  
2           permanent storage for storing a first portion of said encryption key unique to the  
3           apparatus;  
4           a memory for storing a second portion of said encryption key unique to said apparatus;  
5           key logic for forming said encryption key according to a predetermined algorithm from  
6           said first portion and said second portion.

1           16. The apparatus of claim 13, further comprising:  
2           permanent storage for storing a first portion of said encryption key unique to the  
3           apparatus;  
4           a memory for storing a plurality of second portions of said encryption key unique to said  
5           apparatus;  
6           key logic for forming said encryption key to a predetermined algorithm from said first  
7           portion and one certain second portion from said plurality of second portions.

1           17. The apparatus of claim 13, further comprising:  
2           permanent storage for storing a first portion of said encryption key unique to the  
3           apparatus;  
4           a memory for storing a second portion of said encryption key unique to said apparatus;  
5           a user interface for receiving a third portion of said encryption key; and  
6           key logic for forming said encryption key unique to said recording device according to a  
7           predetermined algorithm from said first portion, said second portion, and said third portion,  
8           whereby another user of said apparatus who does not know said third portion is precluded  
9           from obtaining intelligible playback of a recorded data stream.

1           18. The apparatus of claim 13, wherein:

2           a packet comprises a first predetermined number of header bytes and a second  
3           predetermined number of payload bytes;  
4           the data store stores the header bytes unencrypted; and  
5           the data store stores the payload bytes encrypted,  
6           whereby operations performable on the data stream that require access to header bytes  
7           but not to payload bytes are performable on the recorded data stream.

1           19. The apparatus of claim 18, wherein stored packets are retrieved from the  
2           data store, header bytes are not decrypted, and payload bytes are decrypted by the decrypter.

1           20. The apparatus of claim 18, wherein stored packets are retrieved from the  
2           data store, control logic replaces header bytes, and payload bytes are decrypted.

1           21. The apparatus of claim 20, further comprising:  
2           a random-access memory having a plurality of byte locations each corresponding to a  
3           different one of a plurality of first memory addresses;  
4           a FIFO memory sequentially outputting bytes constituting a packet of the decrypted data  
5           stream, wherein:  
6           the output of the FIFO memory is mapped to a number of second memory  
7           addresses at least equal to the number of bytes comprising a packet; and  
8           the second memory addresses are contiguous with the first memory addresses,  
9           and wherein:  
10           the control logic loads replacement header bytes into the n highest locations of the  
11           random-access memory, where n is the first predetermined number; and  
12           the control logic reads out the packet by reading a number of the memory addresses  
13           equal to the sum of the first and second predetermined numbers commencing from the random-  
14           access memory address where the replacement header bytes commence.

1           22. The apparatus of claim 14, comprising a readout path for retrieving at least  
2 a portion of the encryption key and arranged to be disabled by a first irrevocable condition and to  
3 be re-enabled by a second irrevocable condition, and wherein recording and playback functioning  
4 of the apparatus is disabled by said second irrevocable condition.

1           23. The apparatus of claim 22, comprising a one-time-programmable ROM,  
2 and wherein said first irrevocable condition comprises programming ON a first certain bit of the  
3 one-time programmable ROM, and wherein said second irrevocable condition comprises  
4 programming ON a second certain bit of the one-time programmable ROM.

1           24. The method of claim 23, wherein said key portion is stored as bits in the  
2 one-time programmable ROM.

1           25. A recorder for recording digital data streams, encrypted according to a  
2 binary key at least a portion of which is stored in hardware of the recorder, comprising:  
3           a readout path for retrieving said key portion, arranged to be disabled by a first  
4 irrevocable condition and re-enabled by a second irrevocable condition; and  
5           at least one feature essential for recording and playback functioning of the recorder  
6 arranged to be disabled by said second irrevocable condition.

1           26. The recorder of claim 25, comprising a one-time-programmable ROM,  
2 and wherein said first irrevocable condition comprises programming ON a first certain bit of the  
3 one-time programmable ROM, and wherein said second irrevocable condition comprises  
4 programming ON a second certain bit of the one-time programmable ROM.

1           27. The recorder of claim 26, wherein said key portion comprises bits stored  
2 in the one-time programmable ROM.

1           28. A method practiced in a recorder for recording digital data streams,  
2 encrypted according to a binary key at least a portion of which is stored in hardware of the  
3 recorder, the recorder including a readout path for retrieving said key portion, the method  
4 comprising:

5           arranging said readout path to be disabled by a first irrevocable condition and re-enabled  
6 by a second irrevocable condition; and

7           arranging at least one feature essential for recording and playback functioning of the  
8 recorder to be disabled by said second irrevocable condition.

1           29. The method of claim 28, comprising providing in the recorder a one-time-  
2 programmable ROM, and wherein said first irrevocable condition comprises programming ON a  
3 first certain bit of the one-time programmable ROM, and wherein said second irrevocable  
4 condition comprises programming ON a second certain bit of the one-time programmable ROM.

1           30. The method of claim 29 further comprising storing said key portion bits in  
2 the one-time programmable ROM.

3           31. A set-top box for recording and playing back digital data, consisting of a  
4 housing which comprises:

5           a data receiver for receiving packets of a digital data stream;  
6           an encrypter for encrypting the packets according to an encryption key unique to the set-  
7 top box; and  
8           a data store for storing the encrypted packets.

1           32. The set-top box of claim 31, wherein the housing further comprises:  
2           a decrypter for retrieving and decrypting the encrypted packets according to the  
3           encryption key unique to the set-top box; and  
4           a data transmitter for passing the decrypted packets to a presentation device;  
5           whereby a copy of the encrypted packets will not play back intelligibly using another set-  
6           top box,  
7           whereby the encrypted packets are protected against unauthorized distribution.

1           33. The set-top box of claim 31, wherein the housing further comprises:  
2           permanent storage for storing a first portion of said encryption key unique to the set-top  
3           box;  
4           a memory for storing a second portion of said encryption key unique to said set-top box;  
5           key logic for forming said encryption key according to a predetermined algorithm from  
6           said first portion and said second portion.

1           34. The set-top box of claim 31, wherein the housing further comprises:  
2           permanent storage for storing a first portion of said encryption key unique to the set-top  
3           box;  
4           a memory device for storing a plurality of second portions of said encryption key unique  
5           to said set-top box;  
6           key logic for forming said encryption key to a predetermined algorithm from said first  
7           portion and one certain second portion from said plurality of second portions.

## **ABSTRACT OF THE DISCLOSURE**

A recording device for digital data streams, such as digital TV broadcasts or digitized music, stores copies of program content encrypted by a key unique to the recording device. Distribution of program content is thus discouraged, since intelligible playback of program content would not be obtained on another recording device, which would have a different key. To reduce manufacturing complexity which would result from requiring all bits of a key to inhere in hardware, a first portion of the key inheres in hardware and a second portion is selected from among several candidates residing in a memory device, the key being determined by combining the first and second portions according to predetermined rules. The second portion is reselected at predetermined intervals from among the candidates. Only payload portions of packets are encrypted while header portions are left in the clear in order to facilitate ancillary functions of recorder such as fast forward, fast rewind, and program search.

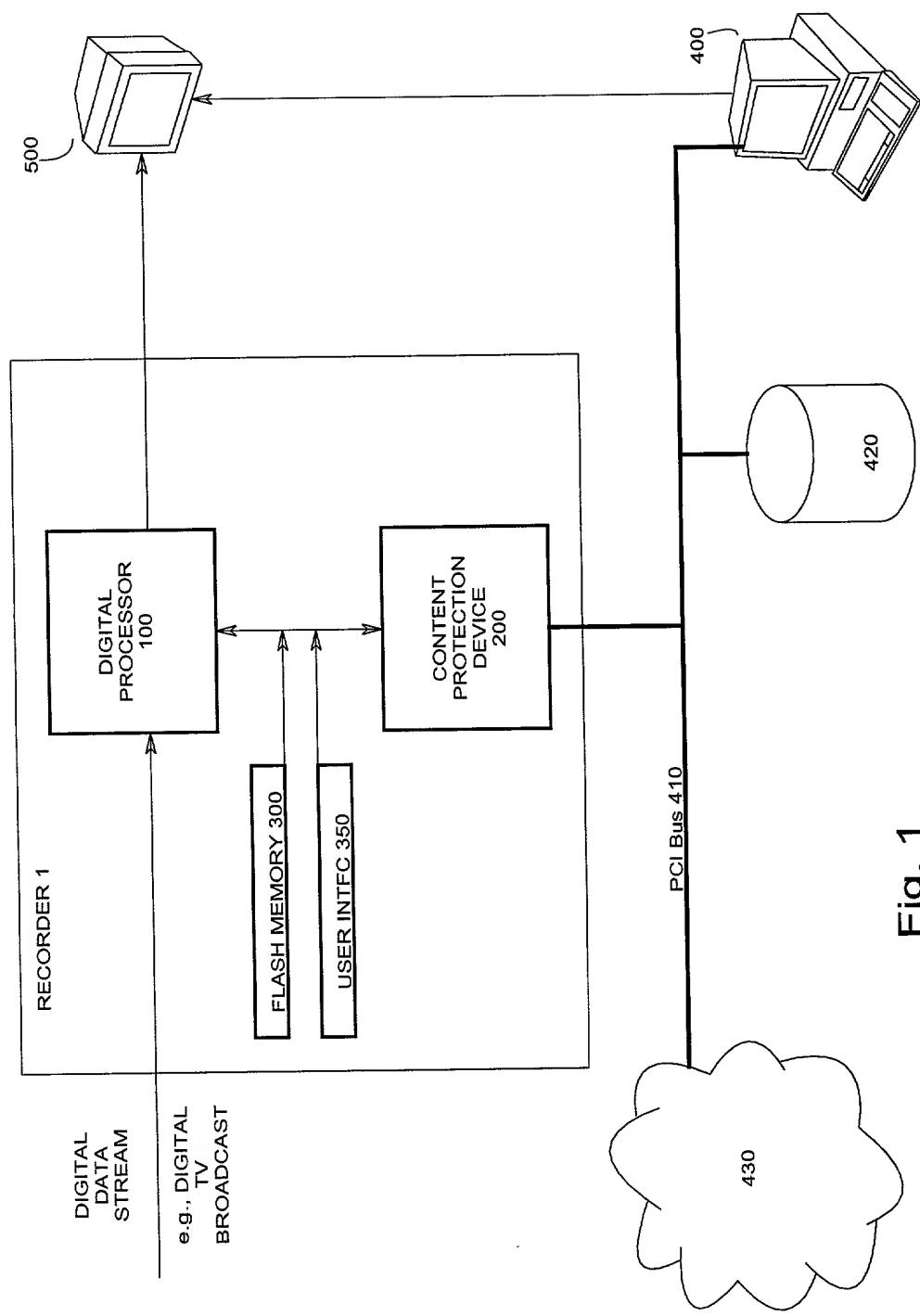


Fig. 1

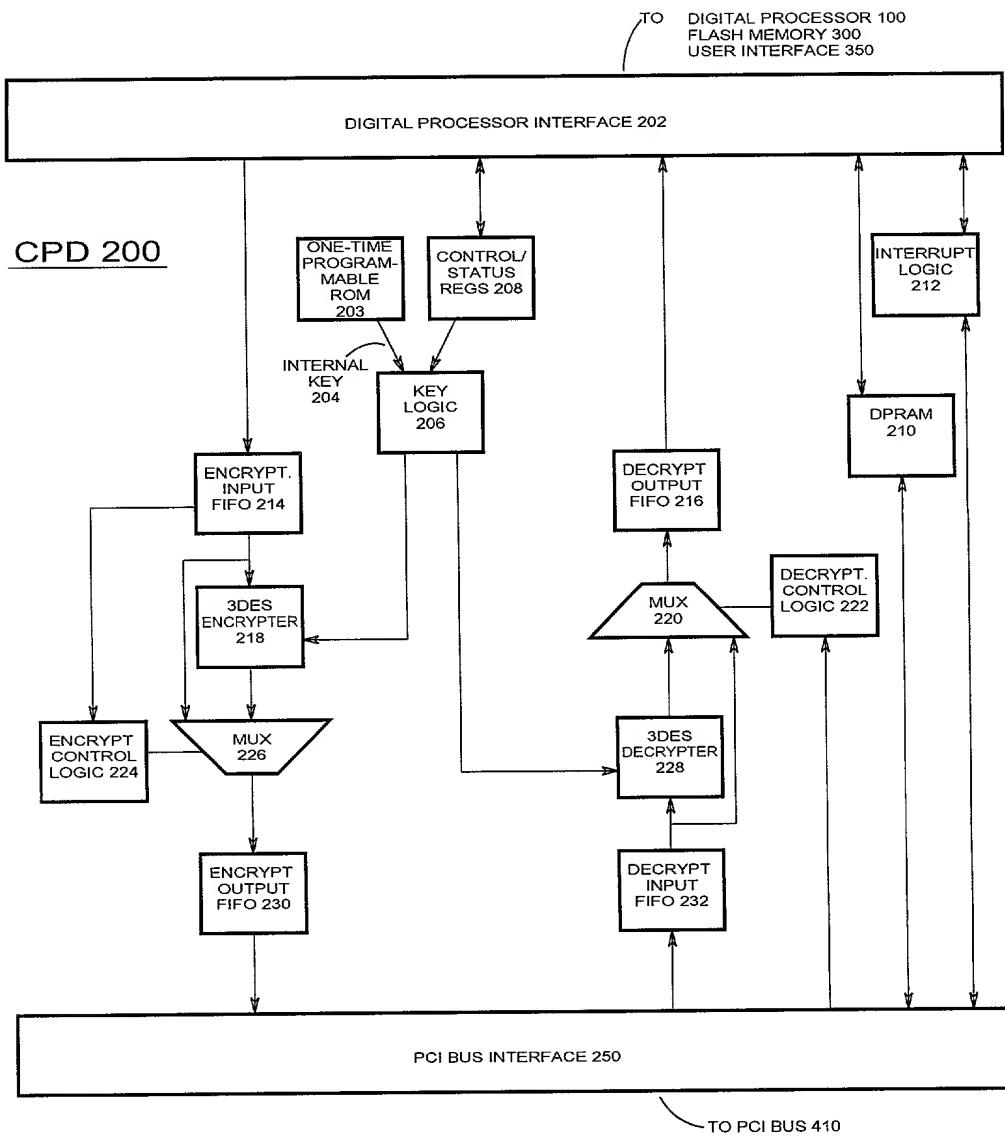
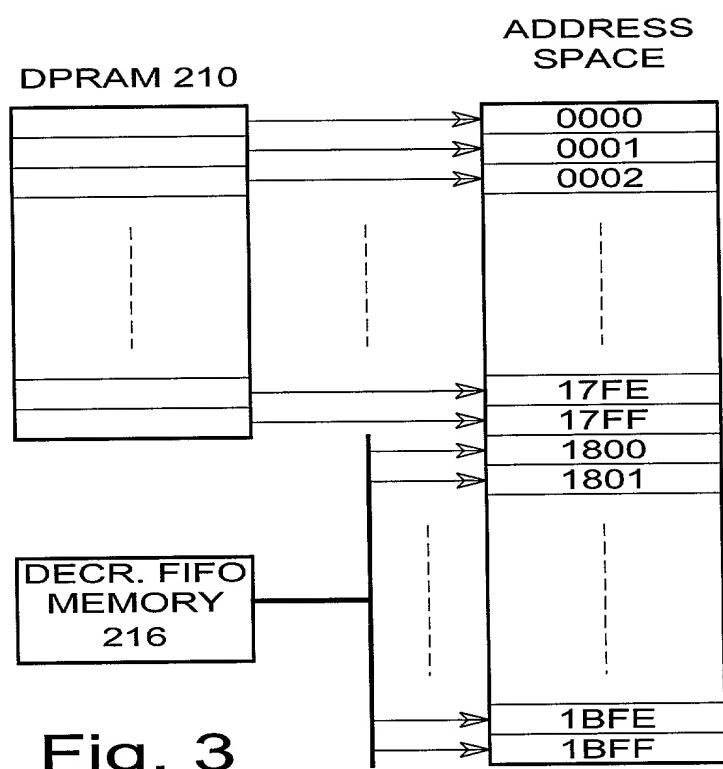
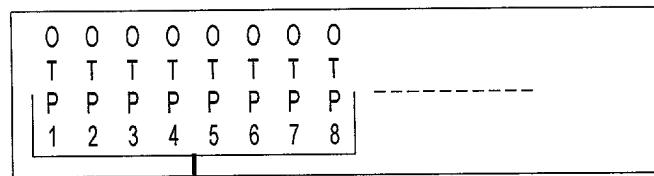


Fig. 2



ONE-TIME PROGRAMMABLE ROM 203



INTERNAL KEY 204  
(8 BITS)

PASS KEY TO KEY LOGIC 206

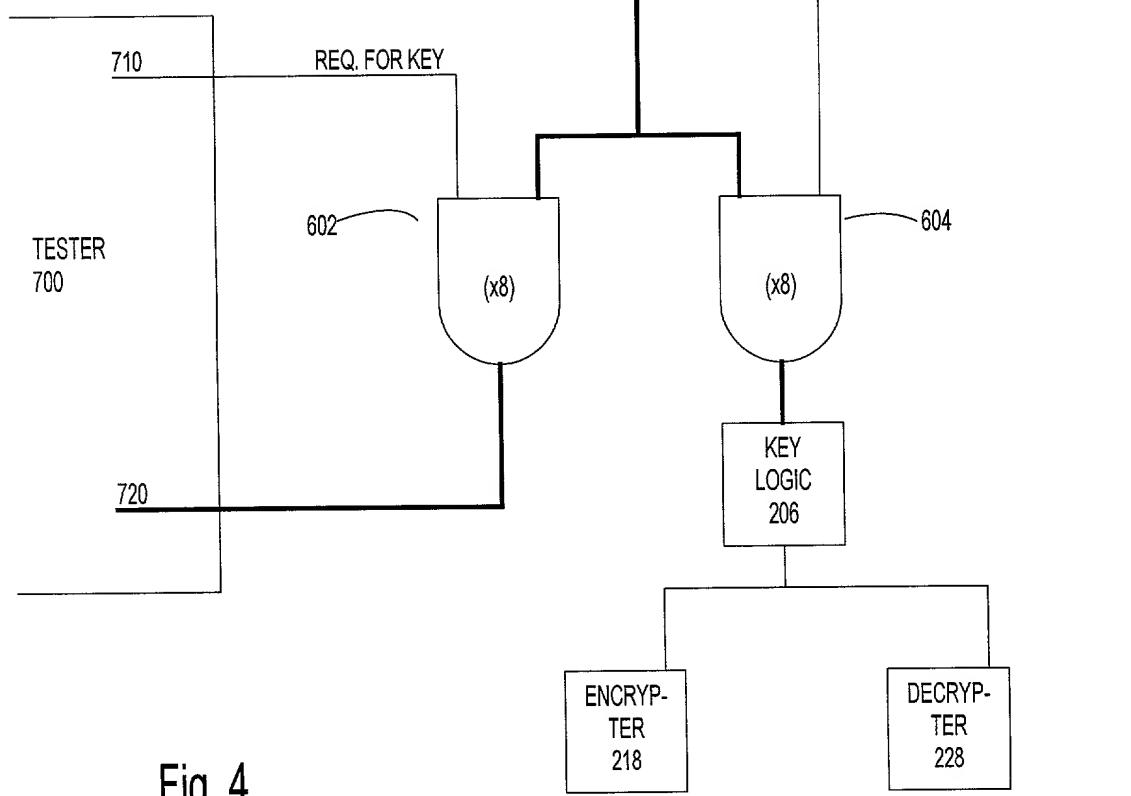


Fig. 4

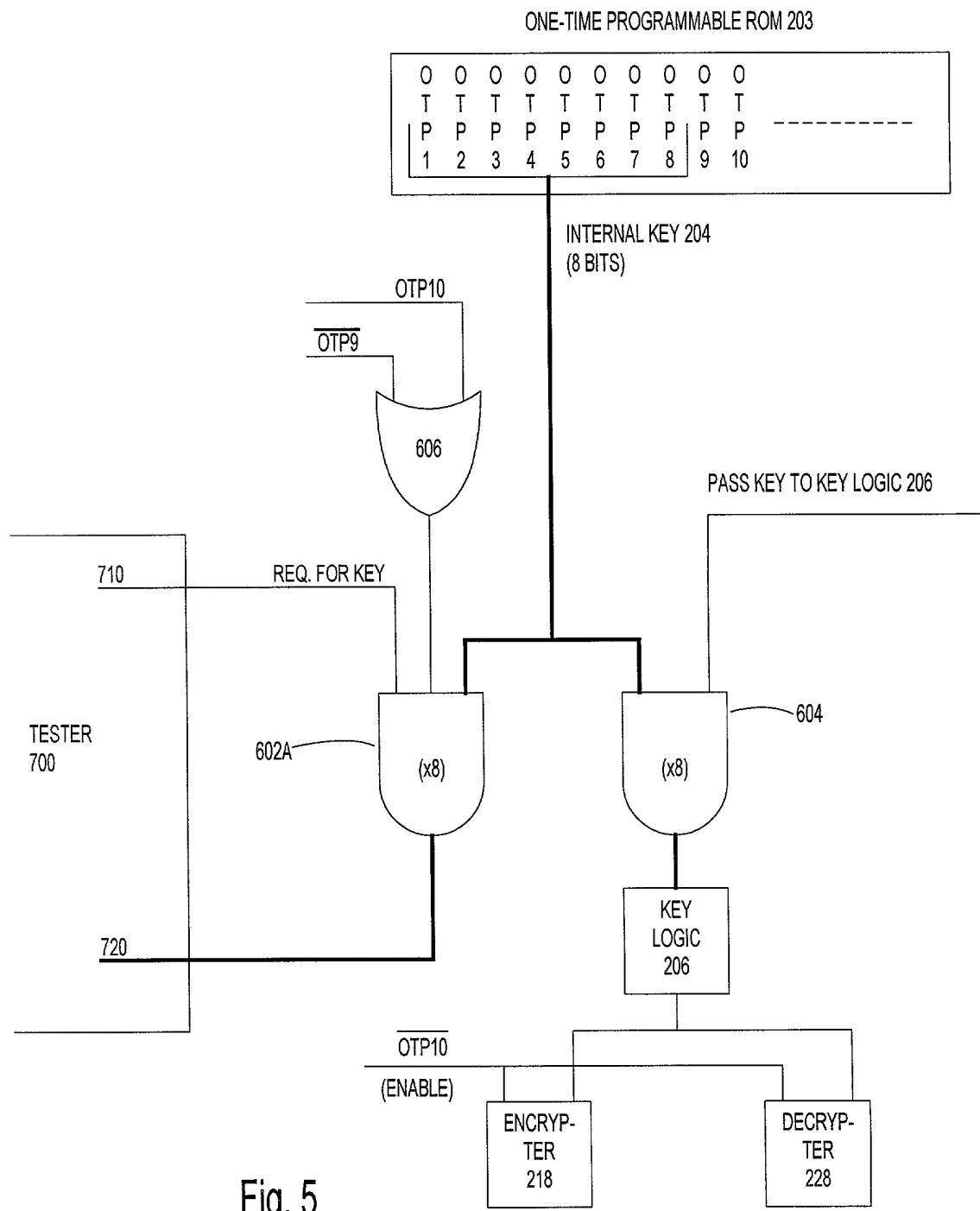


Fig. 5

## **DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

### **CONTENT PROTECTION SCHEME FOR A DIGITAL RECORDING DEVICE**

the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I also acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37 CFR 1.63(d), which occurred between the filing date of the prior application and the filing date of the continuation-in-part application, if this is a continuation-in-part application.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

**Prior Foreign Application:**                   **Country:**  
   **Appln. No.:**  
   **Filed:**

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

MYRON COHEN, Reg. No. 17,358; THOMAS C. PONTANI, Reg. No. 29,763; LANCE J. LIEBERMAN, Reg. No. 28,437; MARTIN B. PAVANE, Reg. No. 28,337; MICHAEL C. STUART, Reg. No. 35,698; KLAUS P. STOFFEL, Reg. No. 31,668; EDWARD M. WEISZ, Reg. No. 37,257; JULIA S. KIM, Reg. No. 36,567; VINCENT M. FAZZARI, Reg. No. 26,879; ALFRED W. FROEBRICH, Reg. No. 38,887; KENT H. CHENG, Reg. No. 33,849; GEORGE WANG, Reg. No. 41,419; TZVI HIRSHAUT, Reg. No. 38,732, GERALD J. CECHONY, Reg. No. 31,335; ROGER S. THOMPSON, Reg. No. 29,594; JOY I. FARBER, Reg. No. 44,103; and GEORGE J. BRANDT, JR., Reg. No. 22,021.

Address all telephone calls to Michael C. Stuart, Esq. at telephone No. (212) 687-2770.

Address all correspondence to:

Michael C. Stuart, Esq.  
Cohen, Pontani, Lieberman & Pavane  
551 Fifth Avenue, Suite 1210  
New York, New York 10176

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Full Name of Sole or First Inventor: Jukka ALVE**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Ida Aalbergin tie 3 a I 14  
Fin-00400 Helsinki, Finland**

Citizenship: **Finnish**

Post Office Address: **Ida Aalbergin tie 3 a I 14  
Fin-00400 Helsinki, Finland**

**Full Name of Second Joint Inventor: Jan MÅRTENSSON**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Björkdalsgatan 30  
SE-59072 Ljungsbro, Sweden**

Citizenship: **Sweden**

Post Office Address: **Björkdalsgatan 30  
SE-59072 Ljungsbro, Sweden**

**Full Name of Third Joint Inventor: Ola LIDHOLM**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Östermalmsgatan 37C  
SE-591 35 Motala, Sweden**

Citizenship: **Sweden**

Post Office Address: **Östermalmsgatan 37C  
SE-591 35 Motala, Sweden**

**Full Name of Fourth Joint Inventor: Valtteri NIEMI**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Topeliuksenkatu 32 G 11  
FIN-00290 Helsinki, Finland**

Citizenship: **Finland**

Post Office Address: **Topeliuksenkatu 32 G 11  
FIN-00290 Helsinki, Finland**

**Full Name of Fifth Joint Inventor: Juha TOMBERG**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Honkatie 27  
FIN-2054 Turku, Finland**

Citizenship: **Finland**

Post Office Address: **Honkatie 27  
FIN-2054 Turku, Finland**

**Full Name of Sixth Joint Inventor: Pasi KÄRKÄS**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Poutamäentie 10B B 18**  
**FIN-000360 Helsinki, Finland**

Citizenship: **Finland**

Post Office Address: **Poutamäentie 10B B 18**  
**FIN-000360 Helsinki, Finland**

**Full Name of Seventh Joint Inventor: Harri PEKONEN**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Upalingonti 64**  
**FIN-21260 Raisio, Finland**

Citizenship: **Finland**

Post Office Address: **Upalingonti 64**  
**FIN-21260 Raisio, Finland**

**Full Name of Eighth Joint Inventor: Rami SUOMINEN**

Inventor's signature: \_\_\_\_\_

Dated: \_\_\_\_\_  
Month/Day/Year

Residence: **Hämeenkatu 8 C 52**  
**FIN-20500 Turku, Finland**

Citizenship: **Finland**

Post Office Address: **Hämeenkatu 8 C 52**  
**FIN-20500 Turku, Finland**

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